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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/807,450	03/24/2004	Tadayoshi Nakano	009683-501	3365
21839	7590	02/13/2006		
EXAMINER				
NGUYEN, LINH V				
ART UNIT		PAPER NUMBER		
		2819		

DATE MAILED: 02/13/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/807,450	NAKANO ET AL.	
	Examiner	Art Unit	
	Linh V. Nguyen	2819	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 01 November 2005.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-17 is/are pending in the application.
 - 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1 is/are rejected.
- 7) Claim(s) 2-17 is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 24 March 2004 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.


REXFORD BARNIE
 SUPERVISORY PATENT EXAMINER

Attachment(s)

- 1) Notice of References Cited (PTO-892)
- 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
 Paper No(s)/Mail Date _____.

- 4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date _____.
- 5) Notice of Informal Patent Application (PTO-152)
- 6) Other: _____.

DETAILED ACTION

1. This office action is in response to communication filed on 11/1/05. Claims 1 – 17 are pending on this application.

Response to Arguments

2. Applicant's arguments filed 11/1/05 have been fully considered but they are not persuasive

With respect to claim 1, under Remark/Arguments applicant argued Kanke et al. does not teach suggest “both internal and A/D conversion circuit receiving the internal voltage”; or “a semiconductor integrated circuit including first and second terminals, a voltage generating circuit, an internal and an A/D conversion circuit and outputting a digital signal to the outside as claimed”, examiner respectfully disagrees from the following:

Since “internal circuit” for using the internal voltage as claimed does not point out any particular structure or device; therefore any electrical/electronic elements (such as the wire, the signal path, or any device in the circuit) which is inputted by the internal voltage is an internal circuit; therefore, Fig. 1 and 7 of Kanke et al. clearly disclosed both an internal circuit (signal path Vin, or integrated circuit 2, using an internal voltage Vin to generate Vout) and A/D conversion circuit (21) receiving the internal voltage Vin by using the internal circuit path Vin, and a semiconductor integrated circuit (4) including first (VB terminal) and second (output terminal of A/D 21) terminals, a voltage generating circuit (3, 1), an internal (2, or signal path 21) and an A/D conversion circuit

(21) and outputting a digital signal (output of A/D 21) to the outside (See Fig. 2 for disclosing the digital output of A/D converter is outputted to the outside of A/D converter).

Per explained above, Kanke et al. from previous office action is applying to this office action.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claim 1 is rejected under 35 U.S.C 102(e) as being anticipated by Kanke et al. (US 6,839,643 B2).

Regarding to claim 1, Figs. 1, 7 and 8B of Kanke et al. teaches a semiconductor integrated circuit (4), comprising: a first terminal (VB) terminal receiving an external power supply voltage (10) provided from outside; a voltage generating circuit (3, 1) lowering (Vref, Vcc) the external power supply voltage (10) and generating an internal voltage (Vin) an internal circuit (signal path Vin, or 2) using said internal voltage Vin (to generating Vout); an A/D conversion circuit (21) and outputting a digital signal (output of A/D 21) to the outside (See Fig. 2 for disclosing the digital output of A/D converter is

outputted to the outside of A/D converter) a second terminal providing said digital signal to the outside (See Fig. 2 for disclosing the output terminal of A/D converter to provide the digital output to the outside of A/D converter).

Allowable Subject Matter

5. Claims 2 - 17 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

6. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Contact Information

Art Unit: 2819

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Linh Van Nguyen whose telephone number is (571) 272-1810. The examiner can normally be reached from 8:30 – 5:00 Monday-Friday. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mr. Rexford Barnie can be reached at (571) 272-7492. The fax phone numbers for the organization where this application or proceeding is assigned are (571-273-8300) for regular communications and (571-273-8300) for After Final communications.


REXFORD BARNIE
SUPERVISORY PATENT EXAMINER

01/24/06

Linh Van Nguyen

Art Unit 2819